

logic of data(binary)
D8,7,8,5,4,3,2,1,0

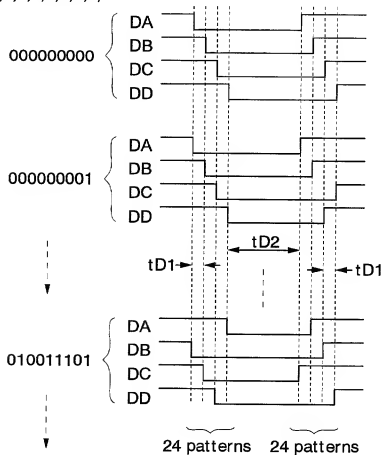


Fig. 1

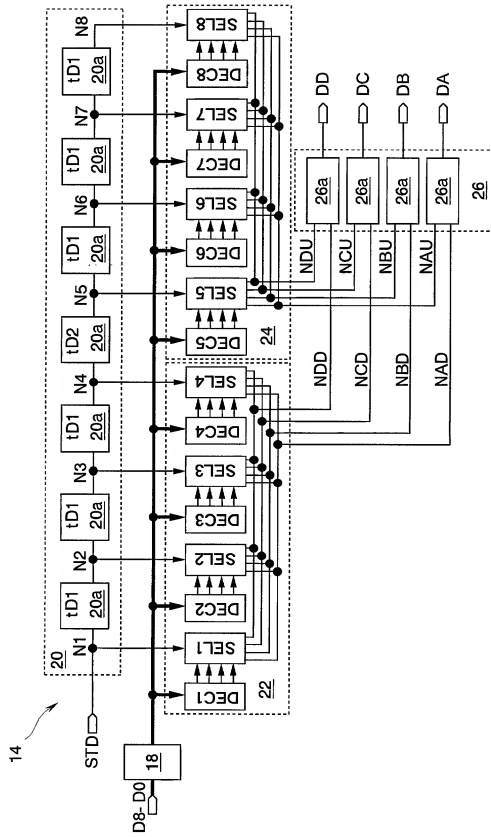


Fig. 2

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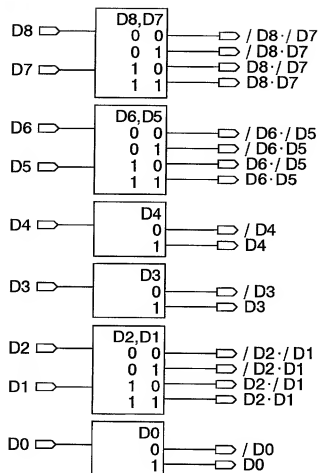


Fig. 3

No.	order of edges	for leading edge					for trailing edge				
		logic L1					logic L2				
		D8,7	D6,5	D4	D3	D2,1	D0	D8,7	D3	D2,1	D0
0	ABCD	00	00	0	11	00	0	11bar	0	00	0
1	ABDC	00	00	1	11	00	1	11bar	0	00	1
2	ACBD	00	01	0	11	01	0	11bar	0	01	0
3	ACDB	00	01	1	11	01	1	11bar	0	01	1
4	ADBC	00	10	0	11	10	0	11bar	0	10	0
5	ADCB	00	10	1	11	10	1	11bar	0	10	1
6	BADC	00	11	0	11	11	0	11bar	0	11	0
7	BADC	00	11	1	11	11	1	11bar	0	11	1
8	BCAD	01	00	0	11	00	0	11bar	1	00	0
9	BCDA	01	00	1	11	00	1	11bar	1	00	1
10	BDAC	01	01	0	11	01	0	11bar	1	01	0
11	BDCA	01	01	1	11	01	1	11bar	1	01	1
12	CABD	01	10	0	11	10	0	11bar	1	10	0
13	CADB	01	10	1	11	10	1	11bar	1	10	1
14	CBAD	01	11	0	11	11	0	11bar	1	11	0
15	CBDA	01	11	1	11	11	1	11bar	1	11	1
16	CDAB	10	00	0	11	00	0	11	-	00	0
17	CDBA	10	00	1	11	00	1	11	-	00	1
18	DABC	10	01	0	11	-	-	11	-	01	0
19	DACB	10	01	1	11	-	-	11	-	01	1
20	DBAC	10	10	0	11	-	-	11	-	10	0
21	DBCA	10	10	1	11	-	-	11	-	10	1
22	DCAB	10	11	0	11	-	-	11	-	11	0
23	DCBA	10	11	1	11	-	-	11	-	11	1

Fig. 4

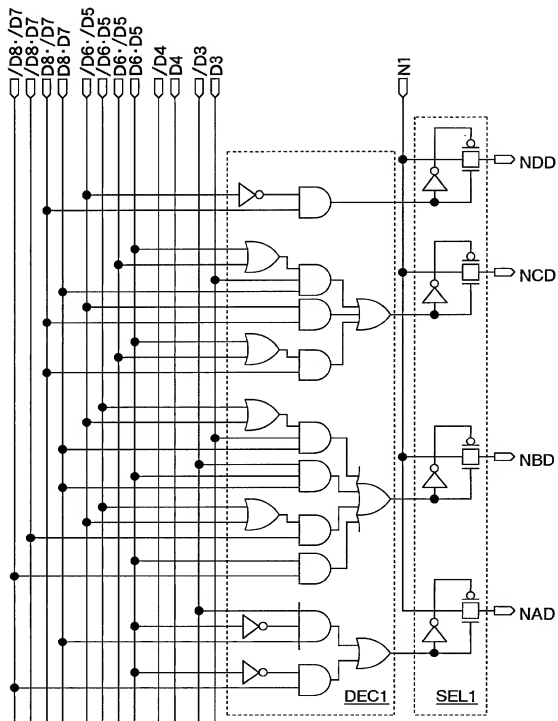


Fig. 5

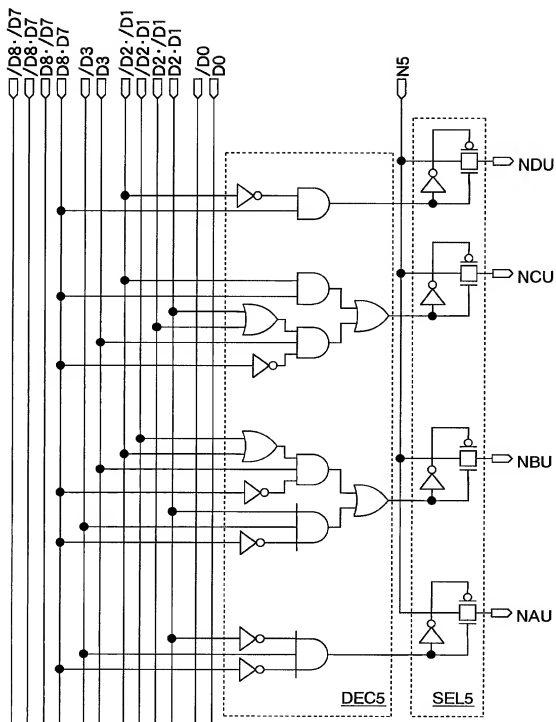


Fig. 6

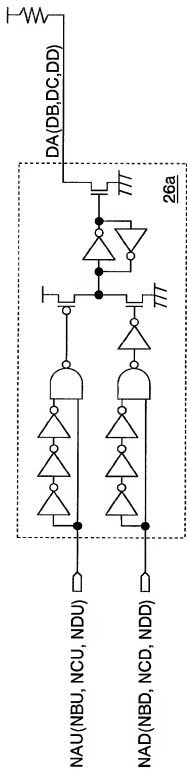


Fig. 7

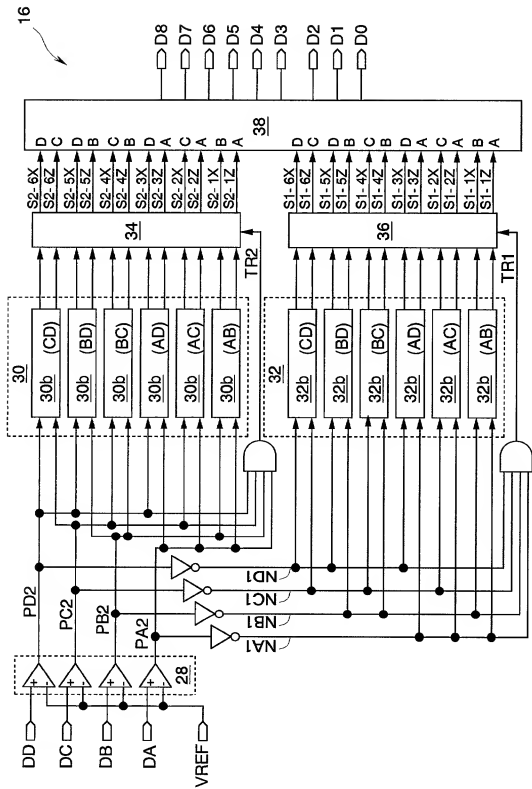


Fig. 8

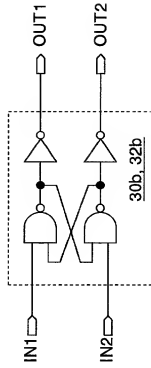


Fig. 9

No.	order of edges	for leading edge										for trailing edge									
		output OUT of comparator 32b					logic L1					logic L2					output OUT of comparator 30b				
		1,2,3,4,5,6	D8,7	D6,5	D4	D3	D8,7	D6,5	D4	D3	D8,7	D6,5	D4	D3	D2,1	D0	1,2,3,4,5,6	D8,7	D3	D2,1	D0
0	ABCD	11111	00	00	0	0	11	00	0	0	11	00	0	0	00	0	11111	11bar	0	00	0
1	ABDC	11110	00	00	1	0	11	00	1	0	11	00	1	0	00	1	11110	11bar	0	00	1
2	ACBD	11101	00	01	0	0	11	01	0	0	11	01	0	0	01	0	11101	11bar	0	01	0
3	ACDB	11100	00	01	1	0	11	01	1	0	11	01	1	0	01	1	11001	11bar	0	01	1
4	ADBC	11100	00	10	0	0	11	10	0	0	11	10	0	0	10	0	11100	11bar	0	10	0
5	ADCB	111000	00	10	0	0	11	10	0	0	11	10	0	0	10	1	111000	11bar	0	10	1
6	BACD	01111	00	11	0	0	11	11	0	0	11	11	0	0	11	0	01111	11bar	0	11	0
7	BADC	01110	00	11	1	0	11	11	1	0	11	11	1	0	11	1	01110	11bar	0	11	1
8	BCAD	00111	01	00	0	1	11	00	0	1	11	00	0	1	00	0	00111	11bar	1	00	0
9	BCDA	00011	01	00	1	1	11	00	1	1	11	00	1	1	00	1	00011	11bar	1	00	1
10	BDAC	010110	01	01	0	1	11	01	0	1	11	01	0	1	01	0	010110	11bar	1	01	0
11	BDCA	000110	01	01	1	1	11	01	1	1	11	01	1	1	01	1	000110	11bar	1	01	1
12	CABD	101011	01	10	0	1	11	10	0	1	11	10	0	1	10	0	101011	11bar	1	10	0
13	CADB	101001	01	10	1	1	11	10	1	1	11	10	1	1	10	1	101001	11bar	1	10	1
14	CBAD	001011	01	11	0	1	11	11	0	1	11	11	0	1	11	0	001011	11bar	1	11	0
15	CBDA	000011	01	11	1	1	11	11	1	1	11	11	1	1	11	1	000011	11	-	00	0
16	CDAB	100001	10	00	0	0	11	00	0	0	11	00	0	0	00	0	100001	11	-	00	1
17	CDBA	000001	10	00	1	0	11	00	1	0	11	00	1	0	01	0	110100	11	-	01	0
18	DABC	110100	10	01	0	0	11	01	0	0	11	01	0	0	01	0	110100	11	-	01	0
19	DACB	110000	10	01	1	0	11	01	1	0	11	01	1	0	01	1	110000	11	-	01	1
20	DBAC	010100	10	10	0	0	11	10	0	0	11	10	0	0	10	0	010100	11	-	10	0
21	DBCA	000100	10	10	1	0	11	10	1	0	11	10	1	0	10	1	000100	11	-	10	1
22	DCAB	100000	10	11	0	0	11	10	0	0	11	10	0	0	11	0	100000	11	-	11	0
23	DCBA	000000	10	11	1	1	11	10	1	1	11	10	1	1	11	1	000000	11	-	11	1

Fig. 10

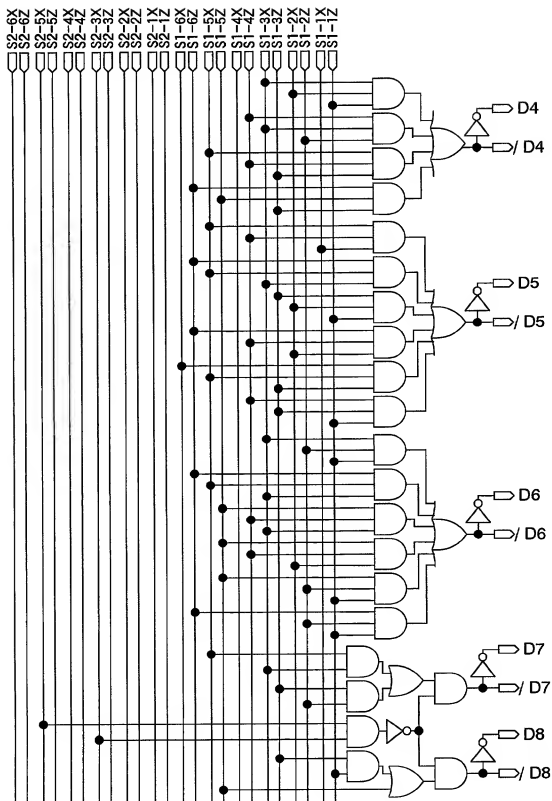


Fig. 11

10003048.120501

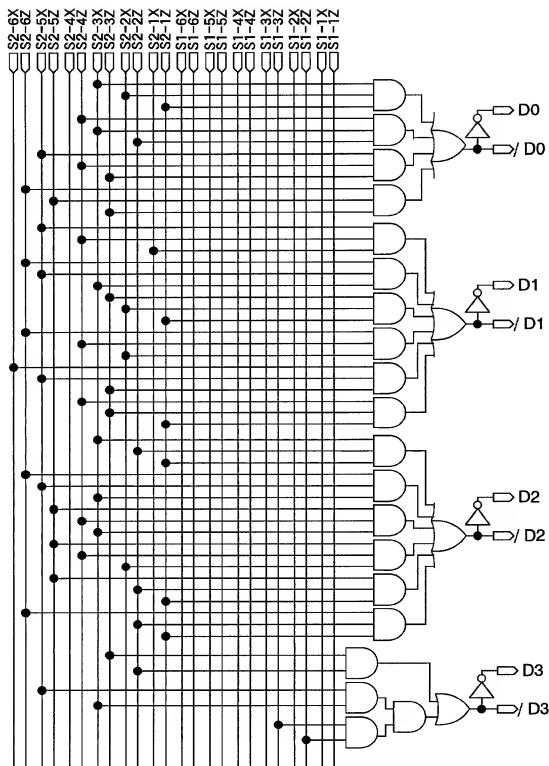


Fig. 12

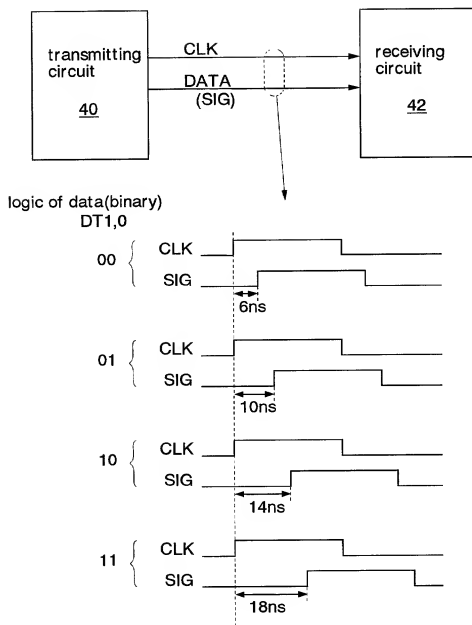


Fig. 13

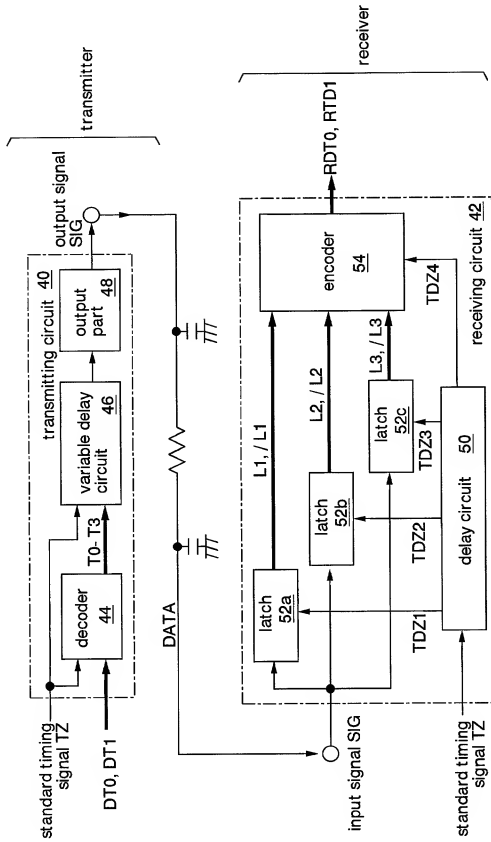


Fig. 14

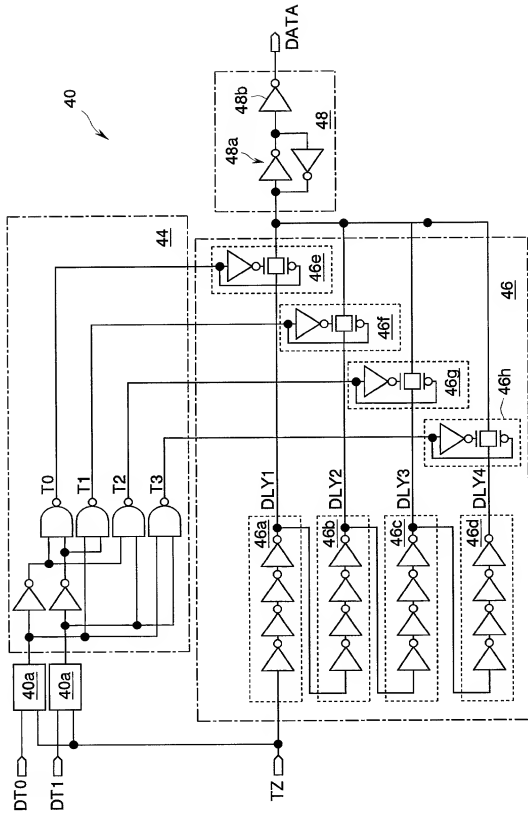


Fig. 15

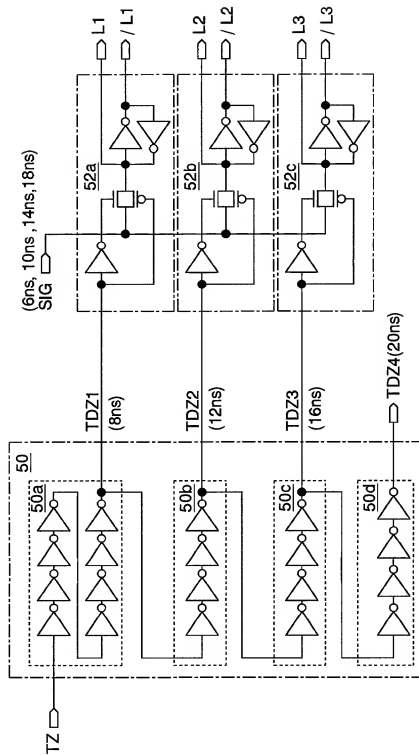


Fig. 16

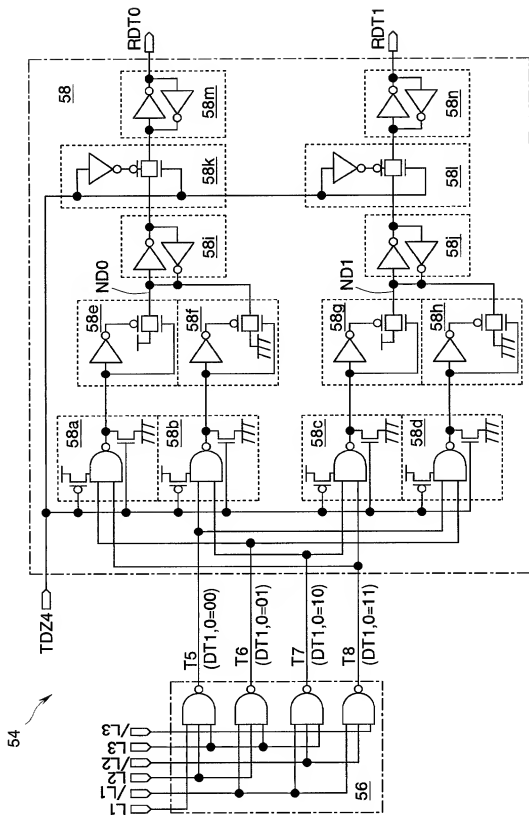


Fig. 17

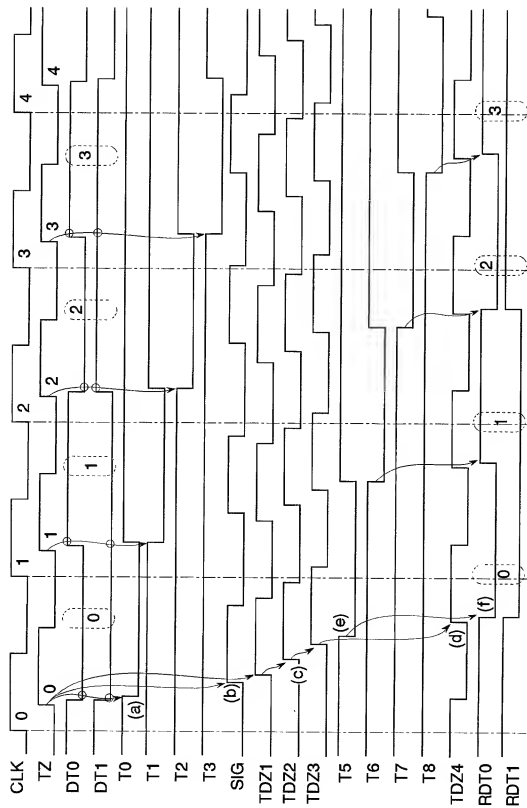


Fig. 18

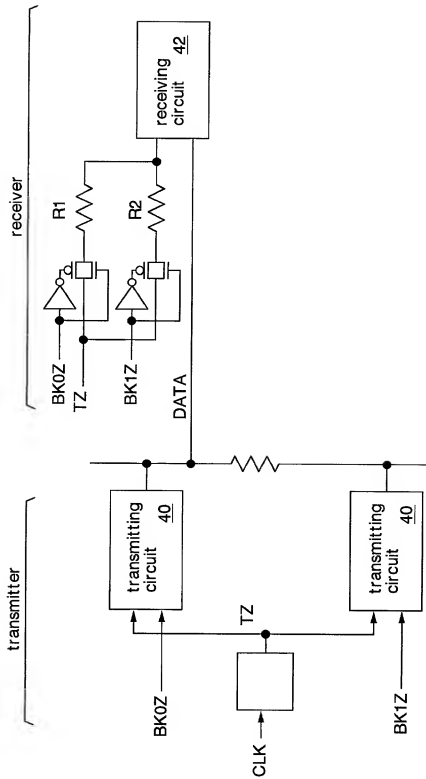


Fig. 19

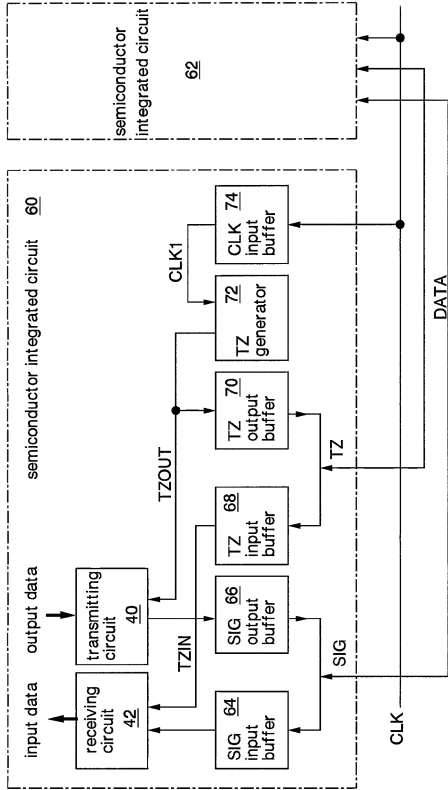


Fig. 20

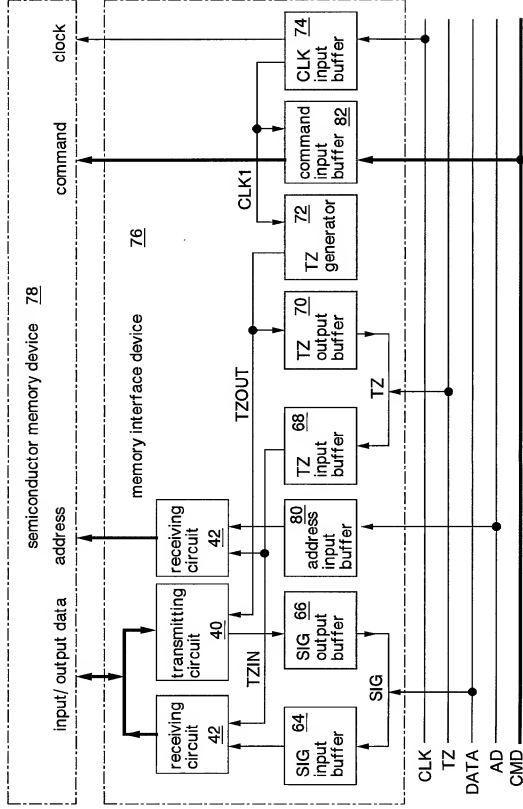


Fig. 21